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FIG. 1

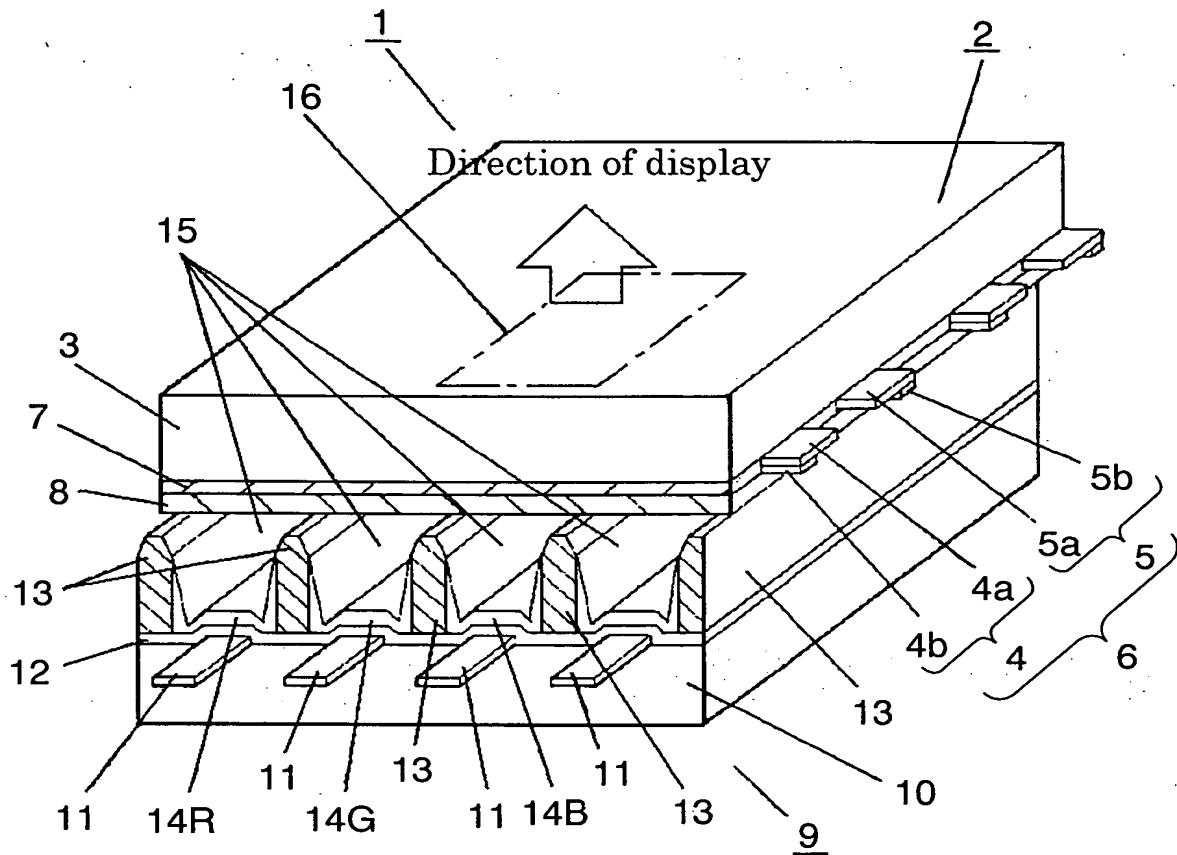
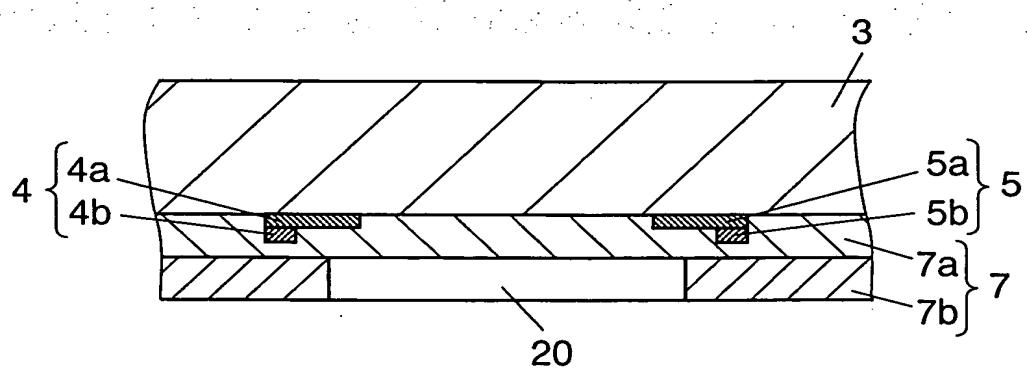
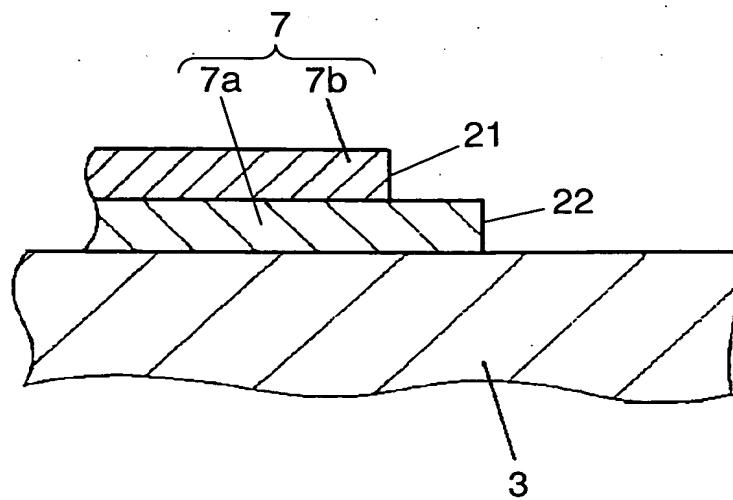
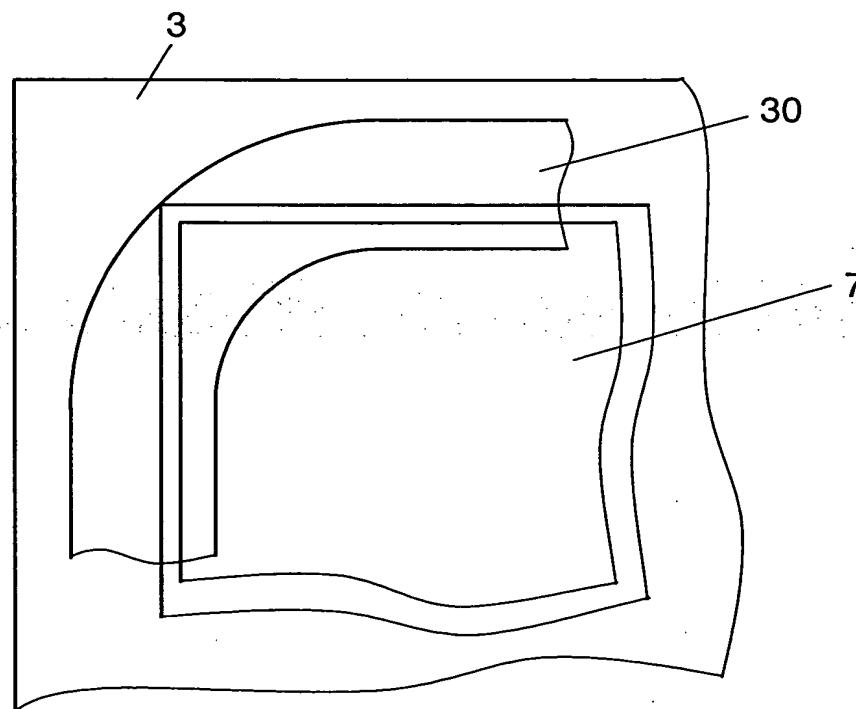


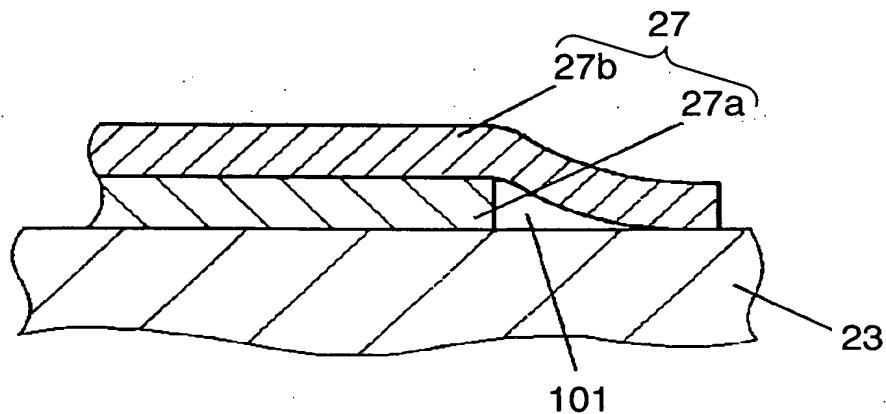
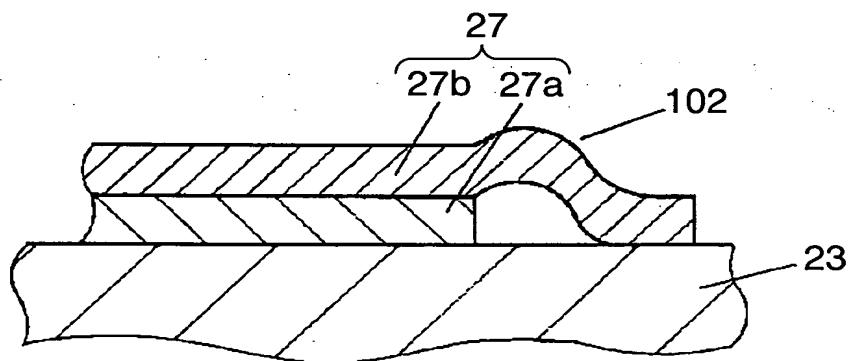
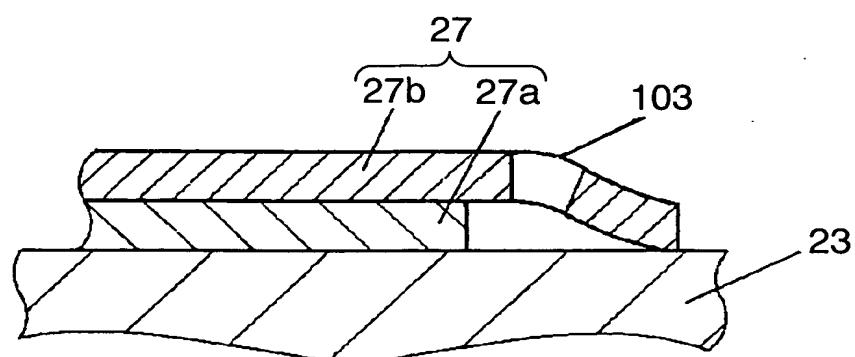
FIG. 2



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FIG. 3**FIG. 4**

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FIG. 5**FIG. 6****FIG. 7**

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Reference marks in the drawings

- 1 Plasma display panel
- 2 Front panel
- 3 Front substrate
- 4 Scanning electrode
- 4a, 5a Transparent electrodes
- 4b, 5b Bus electrodes
- 5 Sustain electrode
- 6 Display electrode
- 7 First dielectric layer
- 7a Lower dielectric layer
- 7b Upper dielectric layer
- 8 Protective layer
- 9 Back panel
- 10 Back substrate
- 11 Data electrode
- 12 Second dielectric layer
- 13 Barrier rib
- 14R, 14G, 14B Phospher layers
- 15 Discharge space
- 16 Discharge cell
- 20 Hole
- 21 Periphery (of upper dielectric layer)
- 22 Periphery (of lower dielectric layer)
- 30 Sealing member